



PPT8200

WAT PARALLEL PARAMETRIC TESTER

▲ Product Description

The PPT8200 is a high-performance parallel parametric test system featuring a "Per-Pin" architecture designed to optimize test efficiency. With a dedicated SMU and PGU for each pin, it achieves over 50% higher throughput compared to traditional serial testers. The modern software environment simplifies test program development and accelerates production ramp-up. It also supports array testing, delivering a solution that combines high reliability with cost-effectiveness.

▲ Key Specifications

Architecture	Per-Pin Architecture
Pin Count	48 pins
Configuration	Per-Pin SMU、Per-Pin PGU
Voltage Range	±200V
Current Range	±1A
Voltage Resolution	100 nV
Current Resolution	1 fA
Throughput	>50% improvement vs. serial testers
Footprint	8.5 m ²

▲ WAT Series

System Category	Model	Voltage/Current (Max)	Resolution (V/I)	Pin Count
Parallel System	PPT8200	200V/1A	100nV/1fA	48 pin
	PPT8300	200V/1A	100nV/1fA	100 pin
Serial System	SPT8000	200V/1A	100nV/10fA	48 pin
	SPT8100	200V/1A	100nV/1fA	48 pin
	SPT8150	200V/1A	100nV/1fA	48 pin
HV (High Voltage)	HPT8000	3500V/1A	100nV/1fA	24 pin
	HPT8100	1100V/1A	100nV/1fA	48 pin
	HPT8200	1800V/1A	100nV/1fA	48 pin
	HPT8300	3500V/1A	100nV/1fA	24 pin