YONATA ELECTRONICS

DCA6065

65 GHz Sampling Oscilloscope

Product Description

The DCA6065 is a high-performance 65 GHz sampling oscilloscope that supports eye diagram testing for ultra-high-speed optical signals of 1.6T/800G/400G. It features a 65 GHz optical port bandwidth with selectable 1/2/4 channel configurations. It integrates a TDECQ feed-forward equalizer (FFE) with up to 64 taps. It supports flexible networking and has a built-in 120 GBaud Clock Data Recovery (CDR), with an optional external CDR module available.

Key Specifications

Optical Bandwidth	65 GHz
Fiber Input	9/125 μm FC/UPC Single-mode
Wavelength Range	1250 ～ 1600 nm
Calibrated Wavelengths	1310/1550 nm ±10 nm
Supported Standard Rates	106.25/112 GBaud PAM4, 53.125/56 GBaud PAM4
ADC Resolution	14 Bit
Max. Input Power	+5 dBm
Average Power Measurement Range	-35 dBm ～ +1 dBm
Input Return Loss	>30 dB @ 1310 nm
Trigger Clock Input Sensitivity	200 mV
Trigger Max Input Signal	±1.5 V
Trigger Input Connector	2.92 mm female
Trigger Clock Frequency Range	500 MHz ～ 15 GHz
Timebase Jitter	130 fs @ typ. (RMS)
Remote Control	Ethernet Socket, USB 2.0/SCPI Command

DCA Series

Model	Application	Bandwidth	Max. Symbol Rate	Signal Support	Wavelength Range	Equalizer (FFE)	Intrinsic Jitter	Channels	Form Factor	Target Speed
DCA6050	Data Center/5G WiFi	30/50 GHz	53 GBaud	NRZ/PAM4	800 - 1650 nm	Standard TDECQ	< 290 fs	1, 2, or 4 ch	Half-Rack	400G/800G
DCA6065	1.6T R&D/AI	65 GHz	112 GBaud	NRZ/PAM4	1250 - 1600 nm	Up to 64 Taps	< 130 fs	1, 2, or 4 ch	Half-Rack	1.6T (224G/L)