



B3224

1.6T BIT ERROR RATIO TESTER

▲ Product Description

B3224 is a high-performance Bit Error Ratio tester (BERT) for physical layer characterization and consistency testing. It covers 1.6TBASE/CEI-224G standards with PAM4 and NRZ signals. Benefiting from excellent signal quality, rich optional features, and flexible testing modes (integrated MCB or remote heads), it ensures strong performance for the research, design, and production testing of high-speed serial circuit products.

▲ Key Specifications

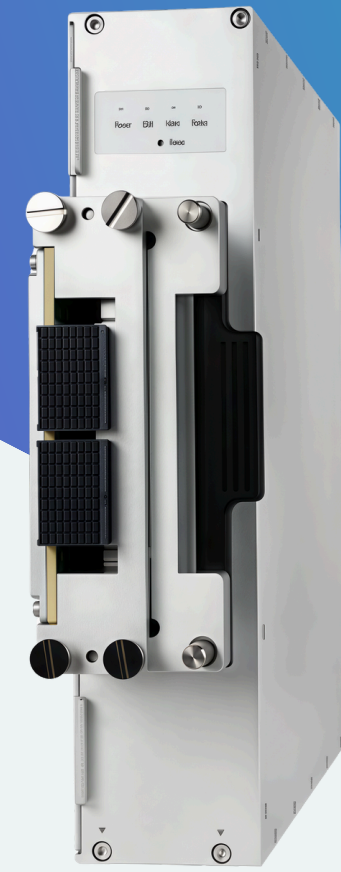
Data Rate	24.33 ~ 120 Gbaud
Output Signal	Differential PAM4/NRZ (AC Coupled)
Output Amplitude	800 mVp-p (Differential)
Rise/Fall Time (20-80%)	< 4.5 ps
Input Signal	Differential PAM4/NRZ (AC Coupled)
Sensitivity	150 mVp-p (Differential)
Test Patterns	PRBS series, SSPRQ, JP03A/B, Square Wave, Custom Defined Pattern
Clock Mode	Built-in Clock Recovery
Connector	MXPD/SMPX
Dimensions (mm)	412 x 441 x 112

▲ BERT Series

Model	Channels	Max. Rate (per Ch)	Application/Product Name	Equipment Type
BB3112	8	112G (50G Burst)	50G Burst Bit Error Analyzer (PON/OLT)	Benchtop
B3112	8	112G	8x112G Bit Error Analyzer (400G/800G)	Benchtop
MB1016K	2	1.6T Total	1.6T Dual-Channel Optical Module Tester	Modular/ATE
MB2016K	4	1.6T Total	1.6T Quad-Channel Optical Module Tester	Modular/ATE
B3224	8	224G	8x224G High-End Bit Error Analyzer (1.6T R&D)	High-End Benchtop
MB2800	4	800G Total	800G Quad-Channel Optical Module Tester	Modular/ATE

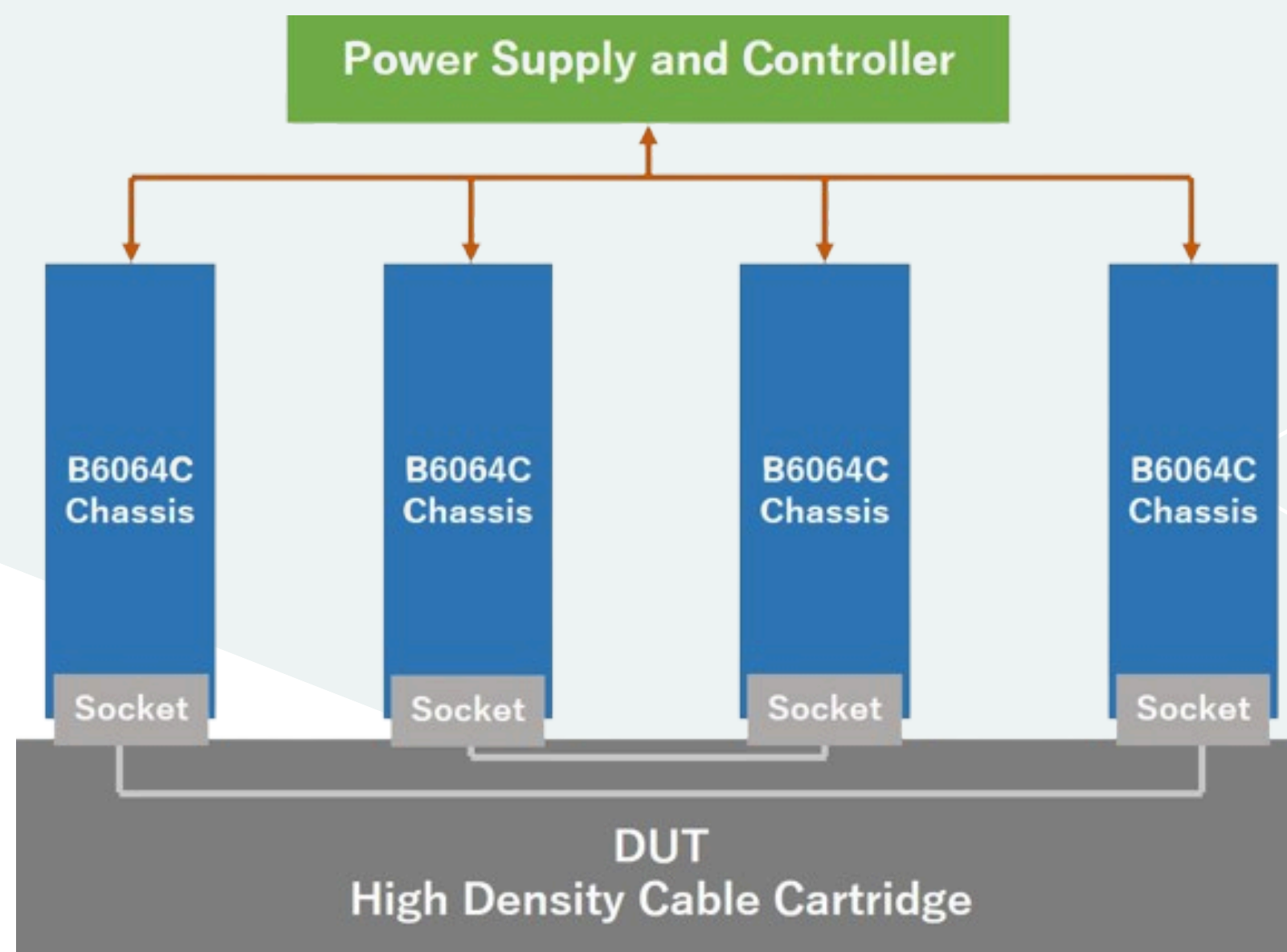
B6064C

PCIe High-Speed Cable BER Tester



▲ Product Description

B6064C is a high-density, versatile solution for Bit Error Ratio (BER) testing of high-speed cables compliant with PCIe 6.0 and earlier standards. Featuring high-density testing of up to 64 lanes (128 differential pairs), automated link equalization training, and robust insertion loss compensation up to 40dB, it is ideal for the R&D, validation, and manufacturing testing of high-speed PCIe cables. This solution enables comprehensive system-level signal integrity verification for data center interconnects and provides critical quality assurance for cable manufacturers and system integrators.



▲ Key Specifications

Test Unit	2 x 64 pairs of TRx
Terminal	AC coupling
Supported Rates	64GT/s, 32GT/s, 16GT/s, 8GT/s, 5GT/s, 2.5GT/s
Protocol Compliance	PCIe 6.0/5.0/4.0/3.0/2.0/1.0
Test Patterns	PRBS7-31
Device Under Test (DUT) Type	High-speed cable at PCIe data rate
System Loss Budget	Die-to-die 40dB @ 64 GT/s (BER < 1E-6)
TX Equalization	4 taps FIR